

INFORMATION DISCLOSURE STATEMENT

Applicants:

Filing Date:

Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
m	4,814,041	03/21/1989	Auda			
	5,196,738	03/23/1993	Takahara et al.			
J	5,269,880	12/14/1993	Jolly et al.			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial	
m	Ohwada et al., "Peripheral Circuit Integrated Poly-Si TFT LCD with Gray Scale Representation", pp. 131-136, 1989, Proceedings of the SID, Vol. 30/2
	Serikawa et al., "Low-Temperature Fabrication of High-Mobility Poly-Si TFT's for Large-Area LCD's", pp. 137-141, 1989, Proceedings of the SID, Vol. 30/2
	Matino et al., "21.1: Horizontal Stripe Color Arrangement for Large-Size TFT-LCD, pp. 400-403, 1990, SID 90 Digest
	US Application Serial No. 09/300,596 filed 4/28/99 (English Specification with Drawings and Claims)
	US Application Serial No. 09/316,198 filed 5/21/99 (English Specification with Drawings and Claims)
	US Application Serial No. 09/961,055 filed 9/24/01 (English Specification with Drawings and Claims)

Date Considered

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initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if
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